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Intel® Firmware Support Package (Intel® FSP) for the Intel® Atom™ Processor
C2000 Product Family Release Notes

Release label: RANGELEY_FSP_POSTGOLD_001_20131218

December 18, 2013

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1. OVERVIEW

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This package contains required binary image(s) and collateral for the Intel® Firmware Support Package (Intel® FSP) for the Intel® Atom™ Processor C2000 Product Family.

This Gold package supports Intel® Firmware Support Package Design Specification v1.0. Please consult the Intel® FSP Integration Guide for further information.

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2. RELEASE INFORMATION

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This Gold release has been integrated into a coreboot Proof of Concept (POC) bootloader and verified on the Mohon Peak Customer Reference Board (CRB).

It has also been validated in the Simics virtual environment.

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3. INTEGRATION NOTES

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Several header and source files are provided in the package to assist with integrating the FSP with the bootloader.

Please consult the Intel® FSP Integration Guide for further information.

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4. CONFIGURATION

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A Binary Configuration Tool for Intel® FSP is provided as a companion tool and is intended to be used to:

- Change configuration options based on provided BSF and FSP.
- Rebase the FSP to a different Base Address.

Please refer to the Binary Configuration Tool User Guide for the usage instructions.

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5. RMT SUPPORT

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To enable Rank Margining Tool (RMT) according to document 535399 RMT User Guid v0.9 (Please contact Intel Representative):

- Open BSF file with BCT
- In North Complex:
 - "Enable Rank Margin Tool" -> Enabled
 - "RMT CPGC exp_loop_cnt" -> 13
 - "RMT CPGC num_bursts" -> 13
- In Platform Settings:
 - "Enable Serial Debug Messages" -> Enabled
- Save and patch the FSP binary file

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6. LIMITATIONS

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- The Serial Port is at port 0x2F8.

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7. CHANGE LOG

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- Rangeley FSP Post Gold Release.

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