



## Fun Filled Explorations at the Intel Museum Summer 2013



Experience the exciting world of silicon technology at the Intel Museum. Explore binary code, bunnysuits, and ultraviolet light, learn how to read a schematic and more with fun filled classes! Explore and learn how silicon is at the heart of an ever expanding, increasingly connected digital world, changing the way people work, learn, play and communicate.

- **Binary Beading** (Grades 2-3) What is binary code and how do computers use it? You'll find out and have a chance to code your initials in a beaded key chain.
- **Science of UV** (Grades 3-8) Explore the science of UV and learn about semiconductor fabrication.
- **Exploring the Clean Room** (Grades 3-8) Explore the world inside a fab - from particles of contamination to bunnysuits - and get an inside glimpse into a Class 1 Fab from the museum.
- **Schematics, Switches and Circuits** (Grades 4-6) Working with wires, batteries, bulbs and switches, students will gain an understanding of electrical circuits. Students will decode basic schematics to build simple series and parallel circuits.

Registration opens June 1, 2013. Space is limited, reservations required. To make a reservation, call

(408) 765-0503 or send an e-mail to [museum@intel.com](mailto:museum@intel.com). Include child's name, age, and telephone number and date and time of the session you would like your child to attend. Reservations close 48 hours in advance of the class. Parents must remain in the museum during the class. Classes last approximately 45-60 minutes. Admission and parking are free. Classes and times are subject to change.

**Intel Museum and Intel Museum Store**

Robert Noyce Building • 2200 Mission College Boulevard • Santa Clara, California 95054 • (408) 765-0503

[www.intel.com/museum](http://www.intel.com/museum)

Date	Class	Grades	Times
Tuesday July 2, 2013	Binary Beading	2 <sup>nd</sup> -3 <sup>rd</sup>	10am, 3pm
Saturday July 6, 2013	Schematics, Switches and Circuits	4 <sup>th</sup> -6 <sup>th</sup>	10am, 3pm
Tuesday July 9, 2013	Exploring the Clean Room	3 <sup>rd</sup> -8 <sup>th</sup>	10am, 3pm
Thursday July 11, 2013	Science of UV	3 <sup>rd</sup> -8 <sup>th</sup>	10am, 3pm
Saturday July 13, 2013	Binary Beading	2 <sup>nd</sup> -3 <sup>rd</sup>	10am, 3pm
Tuesday July 16, 2013	Schematics, Switches and Circuits	4 <sup>th</sup> -6 <sup>th</sup>	10am, 3pm
Thursday July 18, 2013	Exploring the Clean Room	3 <sup>rd</sup> -8 <sup>th</sup>	10am, 3pm
Tuesday July 23, 2013	Science of UV	3 <sup>rd</sup> -8 <sup>th</sup>	10am, 3pm
Thursday July 25, 2013	Binary Beading	2 <sup>nd</sup> -3 <sup>rd</sup>	10am, 3pm
Saturday July 27, 2013	Schematics, Switches and Circuits	4 <sup>th</sup> -6 <sup>th</sup>	10am, 3pm
Tuesday July 30, 2013	Exploring the Clean Room	3 <sup>rd</sup> -8 <sup>th</sup>	10am, 3pm
Thursday August 1, 2013	Science of UV	3 <sup>rd</sup> -8 <sup>th</sup>	10am, 3pm
Tuesday August 6, 2013	Binary Beading	2 <sup>nd</sup> -3 <sup>rd</sup>	10am, 3pm
Thursday August 8, 2013	Schematics, Switches and Circuits	4 <sup>th</sup> -6 <sup>th</sup>	10am
Saturday August 10, 2013	Exploring the Clean Room	3 <sup>rd</sup> -8 <sup>th</sup>	10am, 3pm
Tuesday August 13, 2013	Science of UV	3 <sup>rd</sup> -8 <sup>th</sup>	10am, 3pm
Thursday August 15, 2013	Binary Beading	2 <sup>nd</sup> -3 <sup>rd</sup>	10am, 3pm
Saturday August 17, 2013	Schematics, Switches and Circuits	4 <sup>th</sup> -6 <sup>th</sup>	10am, 3pm
Tuesday August 20, 2013	Exploring the Clean Room	3 <sup>rd</sup> -8 <sup>th</sup>	10am, 3pm
Thursday August 22, 2013	Science of UV	3 <sup>rd</sup> -8 <sup>th</sup>	10am, 3pm

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