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Intel Platform Memory Operations

UDIMM ECC System-Level Validation Results on Future Intel® Xeon® Processor E5 Family

Listed below are the results from a small sample of DDR3 UDIMM ECC tested on future Intel® Xeon® processor E5 family . We are providing this information as a guide to module compatibility with Intel server reference platforms. This testing is not intended to replace the normal OEM qualification process. For results on specific Intel® motherboards or OEM production motherboards, please refer to the OEM's list of qualified memory suppliers.

Updated/New Part

This configuration validated only for the same supplier and same part number across memory channels

UDIMM ECC SR/DR DDR3-1600 1.5V 1DIMM Per Channel / 1Slot Per Channel / 4Channel										
DIMM					DRAM					
Supplier	Part Number	Size	CL	Raw Card	Supplier	Part Number	Density Die	Width	Date Code	Note
Crucial	CT25672BA160B.9FMD	2GB	11	D	Micron	MT41K256M8DA-125:M	2Gb	x8	1129	
Crucial	CT51272BA160B.18FMD	4GB	11	E	Micron	MT41K256M8DA-125:M	2Gb	x8	1129	
Hynix	HMT325U7BFR8C-PB	2GB	11	D	Hynix	H5TQ2G83BFR-PBC	2Gb	x8	1116	
Hynix	HMT351U7BFR8C-PB	4GB	11	E	Hynix	H5TQ2G83BFR-PBC	2Gb	x8	1116	
Hynix	HMT351U7CFR8C-PB	4GB	11	E	Hynix	H5TQ2G83CFR-PBC	2Gb	x8	1133	
Kingston	KVR1600D3E11S/2G	2GB	11	D	Elpida	EDJ2108BCSE-GN-F	2Gb	x8	1112	
Kingston	KVR1600D3E11S/4G	4GB	11	E	Elpida	EDJ2108BCSE-GN-F	2Gb	x8	1052	
Micron	MT9JSF25672AZ-1G6M1	2GB	11	D	Micron	MT41K256M8DA-125:M	2Gb	x8	1129	
Micron	MT18JSF51272AZ-1G6M1	4GB	11	E	Micron	MT41K256M8DA-125:M	2Gb	x8	1129	
Micron	MT18JSF1G72AZ-1G6DZ	8GB	11	E	Micron	MT41J512M8RA-125:D	4Gb	x8	1139	
Samsung	M391B2873GB0-CK0	1GB	11	D	Samsung	K4B1G0846G-BCK0	1Gb	x8	1113	
Samsung	M391B5673GB0-CK0	2GB	11	E	Samsung	K4B1G0846G-BCK0	1Gb	x8	1122	
Samsung	M391B5773DH0-CK0	2GB	11	D	Samsung	K4B2G0846D-HCK0	2Gb	x8	1113	
Samsung	M391B5273DH0-CK0	4GB	11	E	Samsung	K4B2G0846D-HCK0	2Gb	x8	1113	
Samsung	M391B1G73BH0-CK0	8GB	11	E	Samsung	K4B4G0846B-HCK0	4Gb	x8	1113	

Updated on November 10, 2011

Approved Test Labs:

The following test labs have the capability of performing the DDR3 UDIMM ECC system-level testing. For further information please contact:

Platform Memory Operations Lab

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