



Intel® 845MP/MZ Chipset Platform

Design Guide Update

November 2002

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Revision History

Rev.	Draft/Changes	Date
-001	Initial Release	November 2002

Preface

This document is an update to the design guidelines contained in the Mobile Intel® Pentium® 4 Processor-M and Intel® 845MP/MZ Chipset Platform Design Guide. It is intended for hardware system manufacturers. It contains Documentation Changes.

Nomenclature

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

Summary Table of Changes

The following table indicates the Documentation Changes which apply to the listed Mobile Intel® Pentium® 4 Processor-M and Intel® 845MP/MZ Chipset Platform Design Guide.

Shaded:	This item is either new or modified from the previous version of the document.
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NO.	DOCUMENTATION CHANGES
1	Changed THERMTRIP# circuit design recommendation.

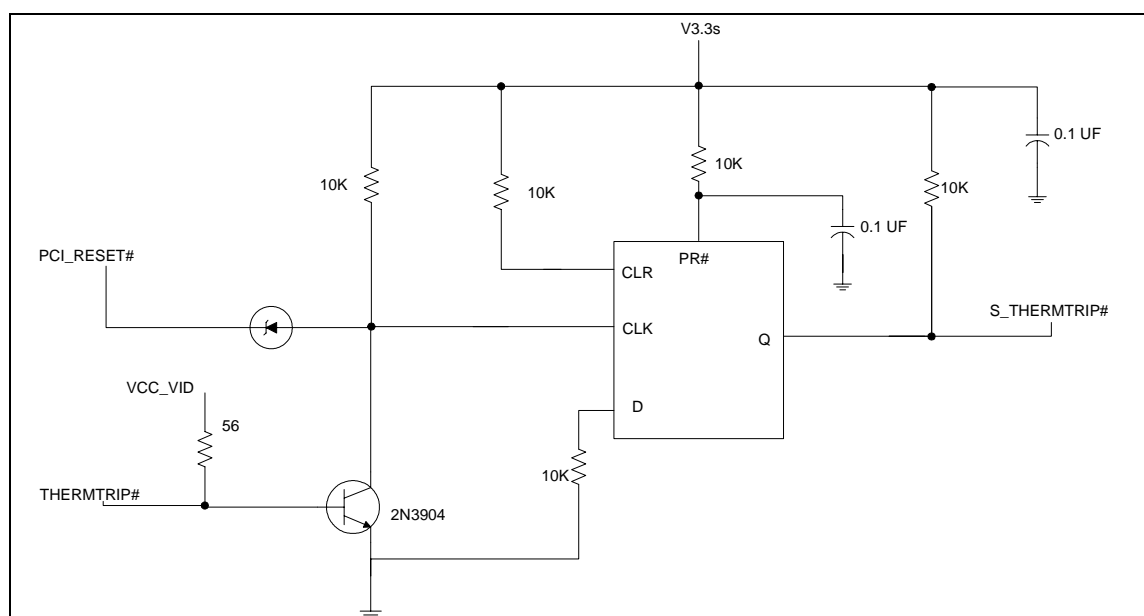
Documentation Changes

1. Replace section 3.4.4.1 with the following:

3.4.4.1. CPU THRMTRIP# Circuit Recommendation

The following sections describe the topologies and layout recommendations for the miscellaneous signals. In Figure 8, the circuit effectively latches the low state of THRMTRIP# via the D Flip-Flop once the processor has driven THRMTRIP# to a low state. Operation can be restored if the system power is cycled (power off/on system).

Figure 8. THRMTRIP# Circuit Recommendation





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