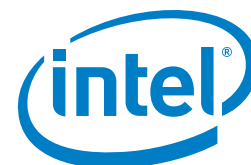


SOLUTION PROFILE

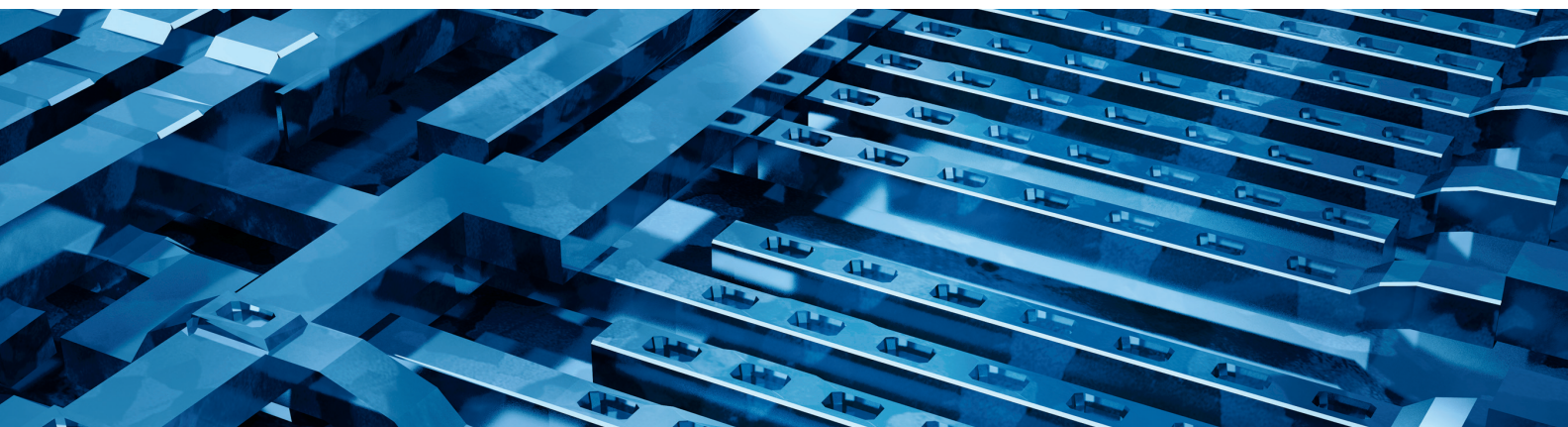
Intel® Xeon® Processor E7 Family

Aspire info Mobile Telecom OSS Solution



Connecting the World with Mobile Data

Aspire info Mobile Telecom OSS Solution meets the demands of extraordinary growth in the Chinese market while driving up efficiency using the Intel® Xeon® processor E7 family.



Solution provided by:



The mobile telecommunications field places a premium on supporting the largest possible number of transactions per server, while also maintaining very high levels of quality. By building performance and efficiency into its products, including the Aspire info Mobile Telecom OSS Solution, Aspire info helps its customers reduce their TCO, creating a positive impact on everyone's success.

CHALLENGE:

Support extremely rapid growth in the number of mobile telecommunications users in the Chinese market with high-quality services that can scale cost effectively as capacity needs grow and also provide efficiencies in terms of both how many users are supported per server and TCO.

SOLUTION:

High optimization of the Aspire info Mobile Telecom OSS Solution for the Intel® Xeon® processor E7 family supports very high numbers of transactions per minute per server. The solution is highly scalable, allowing excellent capacity as more servers are added.

CUSTOMER BENEFIT:

Deploying the Aspire info Mobile Telecom OSS Solution on the Intel Xeon processor E7 family lets some of the leading mobile telecommunications providers in China add customers rapidly and cost effectively. The solution delivers excellent quality and cost effectiveness.

Making a Great Thing Better: Optimization for the Intel® Xeon® Processor E7 Family

Both Aspire info and its customers have found that servers based on the Intel Xeon processor E7 family are an outstanding choice as the target platform for the company's mobile telecommunications solution. The exploding growth of China's mobile user base places extreme demands on server infrastructure, and the scalable performance, reliability, and energy efficiency of these platforms make them an excellent choice.

Of course, where there is opportunity, there is also competition, and that is certainly the case in all aspects of China's telecommunications market. Aspire info meets the competitive challenge by offering highly optimized, high-performance products, and it positions its customers for success through efficient, scalable operations.

To help ensure that its customers will continue to meet the challenges of success in this world of opportunity, Aspire info's solutions are engineered to take advantage of the Intel Xeon processor E7 family, delivering performance 1.21x better than on the Intel® Xeon® processor 7500 series:¹

- **Optimization for the hardware.** Throughout the application life cycle, the team at Aspire info evaluates the value their solution gets from hardware capabilities such as Intel® Hyper-Threading Technology (1.22x improvement¹), and Intel® Turbo Boost Technology (1.039x improvement¹).
- **Direct contact with Intel engineers.** Aspire info makes excellent use of technical and business disclosures provided by Intel application engineers to ensure that Aspire info products are ready to get the full benefit of current and upcoming Intel® platforms.
- **A focus on customers' bottom lines.** The quality and platform optimization built into the Aspire info solution helps ensure flexible, cost-effective support for large numbers of end-users, and the Intel Xeon processor E7 family helps control energy costs, with 1.37x better performance per watt¹ for the Aspire info Mobile Telecom OSS Solution, compared to the Intel Xeon processor 7500 series.

As mobile connectivity continues to proliferate in China, Aspire info's customers are positioned to help spread access and quality of life through technology.

Engines of Change: The Intel® Xeon® Processor E7 Family

The Intel Xeon processor E7 family extends the limits of scalable performance, reliability, security, and energy efficiency for enterprise servers:

- **Scalable Performance.** Up to 10 cores (20 threads), support for 32-GB DDR3 DIMMs (2 TB per four-socket system),² and 30 MB of last-level cache.
- **Reliability and Security.** Intel® Advanced Encryption Standard New Instructions (Intel® AES-NI), Intel® Trusted Execution Technology, Double Device Data Correction (DDDC), and Partial Memory Mirroring.
- **Energy Efficiency.** More performance within the same power envelope as predecessors, Intel® Intelligent Power Technology,³ and low-voltage DIMM support.⁴

Learn more about Aspire info Mobile Telecom OSS Solution: www.aspire-info.com

Learn more about the Intel® Xeon® processor E7 family: www.intel.com/xeon

¹ Internal Intel testing. Test configurations:

Intel® Xeon® processor E7 family running at 2.26 GHz, 64-GB DDR3-1333 LV RAM, two SAS hard disks in RAID-0 configuration.

Intel® Xeon® processor 7500 series running at 2.26 GHz, 64-GB DDR3-1066 RAM, two SAS hard disks in RAID-0 configuration.

² Up to 64 slots per standard 4-socket system x 32 GB/DIMM = 2 TB.

³ Uses similar core and package C6 power states enabled on Intel® Xeon® processor 5500 and 5600 series. Requires OS support.

⁴ Savings dependent on workload and configuration. Example: At 100-percent SPECpower® load it can save ~0.8W for 4-GB DIMM DRx8 based on early Intel internal estimates.

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products.

Intel® compilers, associated libraries, and associated development tools may include or utilize options that optimize for instruction sets that are available in both Intel® and non-Intel microprocessors (for example SIMD instruction sets) but do not optimize equally for non-Intel microprocessors. In addition, certain compiler options for Intel compilers, including some that are not specific to Intel® microarchitecture, are reserved for Intel microprocessors. For a detailed description of Intel compiler options, including the instruction sets and specific microprocessors they implicate, please refer to the "Intel® Compiler User and Reference Guides" under "Compiler Options." Many library routines that are part of Intel® compiler products are more highly optimized for Intel microprocessors than for other microprocessors. While the compilers and libraries in Intel compiler products offer optimizations for both Intel and Intel-compatible microprocessors, depending on the options you select, your code, and other factors, you likely will get extra performance on Intel microprocessors.

Intel compilers, associated libraries, and associated development tools may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include Intel® Streaming SIMD Extensions 2 (Intel® SSE2), Intel® Streaming SIMD Extensions 3 (Intel® SSE3), and Intel® Supplemental Streaming SIMD Extensions 3 (Intel® SSSE3) instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors.

While Intel believes our compilers and libraries are excellent choices to assist in obtaining the best performance on Intel and non-Intel microprocessors, Intel recommends that you evaluate other compilers and libraries to determine which best meet your requirements. We hope to win your business by striving to offer the best performance of any compiler or library; please let us know if you find we do not. Notice revision #20101101

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