



# Intel<sup>®</sup> Desktop Board DX48BT2 Specification Update

March 2008

Order Number: E39394-001US

The Intel<sup>®</sup> Desktop Board DX48BT2 may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

# Revision History

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Revision	Revision History	Date
-001	This document is the first Specification Update for the Intel® Desktop Board DX48BT2.	March 2008

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The Intel® Desktop Board DX48BT2 may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications before placing your product order.

Copies of documents which have an ordering number and are referenced in this document, or other Intel literature, may be obtained from:

Intel Corporation  
P.O. Box 5937  
Denver, CO 80217-9808

or call in North America 1-800-548-4725, Europe 44-0-1793-431-155, France 44-0-1793-421-777, Germany 44-0-1793-421-333, other Countries 708-296-9333

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# Specification Update for the Intel<sup>®</sup> Desktop Board DX48BT2

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This document is an update to the specifications contained in the *Intel<sup>®</sup> Desktop Board DX48BT2 Technical Product Specification* (Order Number E29116). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

For specification updates concerning the Intel processor that may apply to this desktop board, refer to the following:

- Intel<sup>®</sup> Core<sup>™</sup>2 Extreme Processor X6800Δ and Intel<sup>®</sup> Core<sup>™</sup>2 Duo Desktop Processor E6000Δ and E4000Δ Sequences Datasheet (Order Number 313279)
- Intel<sup>®</sup> Core<sup>™</sup>2 Duo Processor E8000 Series Datasheet (Order Number 318733)
- Intel<sup>®</sup> Core<sup>™</sup>2 Extreme Quad-Core Processor QX6000Δ and Intel<sup>®</sup> Core<sup>™</sup>2 Quad Processor Q6000Δ Sequence Datasheet (Order Number 315593)
- Intel<sup>®</sup> Core<sup>™</sup>2 Extreme Processor QX9650Δ and Intel<sup>®</sup> Core<sup>™</sup>2 Quad Processor Q9000Δ Series Datasheet (Order Number 318727)

Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel<sup>®</sup> X48 Bear Lake Chipset Specification Update* (Order Number 351565) for specification updates concerning the 82X48 MCH Controller and that may apply to the desktop board DX48BT2. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel<sup>®</sup> IO Controller Hub (ICH9R) Family Specification Update* (Order Number 316973) for specification updates concerning the 82801IR I/O Controller Hub and that may apply to the desktop board DX48BT2. Unless otherwise noted in this document, it should be assumed that any ICH9R errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

## Terminology

**Specification Changes** are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

**Errata** are design defects or errors. Characterized errata may cause the desktop board behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

**Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

**Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

## General Information

### Basic Desktop Board DX48BT2 Identification Information

AA Revision	BIOS Revision	Notes
D85848-202	BTX3810J.86A.1513.2008.0304.1726	1-2

Notes:

1. The AA number is found on a small label on the component side of the board.
2. The X48 Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82X48	A1	LASF
82801IR	A2	LA9N

## Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board DX48BT2. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

### Codes Used in Summary Table

Doc:	Document change or update that will be implemented.
PlanFix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	SPECIFICATION CHANGES
		There is no characterized erratum for this product.