



# Intel<sup>®</sup> Desktop Board DQ35MP Specification Update

May 2008

Order Number E21493-002US

The Intel<sup>®</sup> Desktop Board DQ35MP may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

# Revision History

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Revision	Revision History	Date
-001	This document is the first Specification Update for the Intel® Desktop Board DQ35MP	August 27, 2007
-002	Updated to General Information and Specification Changes Sections	May 2008

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The Intel® Desktop Board DQ35MP may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications before placing your product order.

Copies of documents which have an ordering number and are referenced in this document, or other Intel literature, may be obtained from:

Intel Corporation  
P.O. Box 5937  
Denver, CO 80217-9808

or call in North America 1-800-548-4725, Europe 44-0-1793-431-155, France 44-0-1793-421-777, Germany 44-0-1793-421-333, other Countries 708-296-9333

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# Specification Update for the Intel® Desktop Board DQ35MP

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This document is an update to the specifications contained in the *Intel® Desktop Board DQ35MP Technical Product Specification* (Order Number D96938). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

For specification updates concerning the Intel processor that may apply to this desktop board, refer to the following:

- *Intel® Core™2 Quad Desktop Processor Q6600Δ Specification Update* (Order Number: 315593)
- *Intel® Core™2 Duo Desktop Processor E6000Δ Sequence Specification Update* (Order Number: 313279)
- *Intel® Pentium® Dual-Core Desktop Processor E2000Δ Sequence Specification Update* (Order Number: 316982)
- *Intel® Celeron® 400Δ Sequence Specification Update* (Order Number: 316964)

Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® Q35 Bearlake Chipset Specification Update* (Order Number: Not Yet Available) for specification updates concerning the 82Q35 GMCH Controller and that may apply to the desktop board DQ35MP. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® IO Controller Hub 9 (ICH9) Family Specification Update* (Order Number: 316973) for specification updates concerning the 828011DO I/O Controller Hub and that may apply to the desktop board DQ35MP. Unless otherwise noted in this document, it should be assumed that any ICH9 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

# Terminology

**Specification Changes** are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

**Errata** are design defects or errors. Characterized errata may cause the desktop board behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

**Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

**Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

# General Information

## Basic Desktop Board DQ35MP Identification Information

AA Revision	BIOS Revision	Notes
D82086-700	JOQ3510J.86A.0546	1,2
D82086-701	JOQ3510J.86A.0559	1,2
D82086-702	JOQ3510J.86A.0559	1,2
D82086-800	JOQ3510J.86A.0559	1,2
D82086-801	JOQ3510J.86A.0559	1,2
D82086-802	JOQ3510J.86A.0865	1,2

Notes:

1. The AA number is found on a small label on the component side of the board.
2. The Q35 Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82Q35	A2	SLAEX
828011DO	A2	SLAFD

## Summary of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board DQ35MP. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted.

The following notations are used in the table:

Doc:	Document change or update that will be implemented.
PlanFix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

No.	Plans	Specification Changes
1	Doc	Section 1.6.3.3 has been added to the Technical Specification.
No.	Plans	Errata
		There are no characterized erratum for this product

## Specification Changes

Changes to the feature description in the board technical product specification. Typically the customer does not have to do anything to achieve proper device functionality as a result of the change.

1. Section 1.6.3.3 has been added to the Technical Specification.

### 1.6.3.3 Intel® Rapid Recover Technology

The Intel® Desktop Board DQ35MP incorporates the Intel® Rapid Recover Technology. Intel Rapid Recover Technology is a feature of Intel® Matrix Storage Manager. It uses RAID 1 (mirroring) functionality to copy data from a designated master drive to a designated recovery drive. The master drive data can be copied to the recovery drive either continuously or on request.

When using the continuous update policy, changes made to the data on the master drive while the recovery drive is disconnected or offline are automatically copied to the recovery drive when it is reconnected. When using the on request update policy, the master drive data can be restored to a previous state by copying the data on the recovery drive back to the master drive. The table below provides an overview of the advantages, disadvantages, and the typical usage of Intel Rapid Recover Technology.

<b>Hard Drives Required</b>	2
<b>Advantage</b>	More control over how data is copied between master and recovery drives; fast volume updates (only changes to the master drive since the last update are copied to the recovery drive); member hard drive data can be viewed in Microsoft Windows* Explorer.
<b>Disadvantage</b>	No increase in volume capacity.
<b>Application</b>	Critical data protection; fast restoration of the master drive to a previous or default state.